

Features and Benefits

Better than ± 500 ppb from -40°C to +85°C 10 MHz low noise LVCMOS output 3.3V supply; 3.5 mA typical Less than -145 dBc/Hz @ 1KHz offset

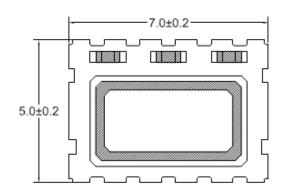
Less than -145 dBc/Hz @ 1KHz offset Less than -154 dBc/Hz @ 10KHz offset

Typical Applications

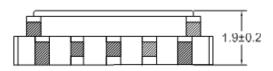
Mobile SATCOM Mobile Radio Harsh Environments Femto-cell

Mechanical Drawing

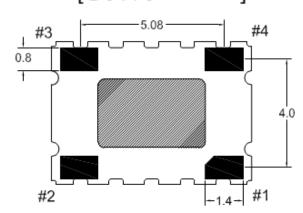
[TOP VIEW]



[SIDE VIEW]



[BOTTOM VIEW]



PIN	FUNCTION				
#1	Vcon VC-TCXO				
	GND TCXO				
#2	GND				
#3	OUTPUT				
#4	VDD				

TCXO7500T-10MHz-B



Specifications

TCXO Specification	Sym	Condition	Value			Unit	Note
	Oyiii		Min.	Ту	Max.	Offic	NOLE
Operational Frequency Range	f_0			10.000000		MHz	
LVCMOS		Load		15		pF	
		Logic 1 level	0.9Vcc			V	
		Logic 0 level			0.1Vcc	V	
		Duty Cycle	45	50	55	%	
Power Supply							
Voltage	V _{CC}		2.970	3.300	3.630	V	
Current Consumption				3.5	6.0	mA	
Frequency versus Voltage							
Pin 1: Voltage Control		1.65V +/- 1.35V		+/- 8.0		ppm	
Pin 1: Input Impedance				100		K ohm	
Frequency Stability							
Vs. Temperature		-40°C to +85°C			+/-0.500	ppm	
Vs. at 25°C	Initial A	Initial Accuracy at time of shipment			+/-0.500	ppm	
Vs. Reflow Shift	Aft	After 24 hours settling time			+/-1.000	ppm	
Aging							
	Aft	After 30 Days of Operation			+/- 1.0	ppm	
SSB Phase Noise							
@ 10MHz		100 Hz		-130			
		1 KHz		-145		dBc/Hz	
		10 KHz		-154	-		
		100 KHz		-154			