## Dynamic Engineers Inc.

2550 Gray Falls Dr., Suite#128, Houston, TX, 77077 USA TEL: 1-281-870-8822 EMAIL:Sales@DynamicEng.com

H7 LC+) \$\$N!(, A < n!5!J Pāt @ÁÚcæàājāc Á, āc@ÁÚ`]^¦āj¦ÁOEļæ; Áå^çāææāj}

### Features and Benefits

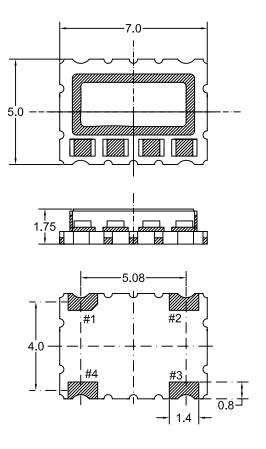
High frequency stability(up to  $\pm 0.3$ ppm over -40°C to +85°C) 1 x 10<sup>-10</sup> short term stability ADEV (at tau = 1 sec) SMD package design

Typical Applications
Microwave Communication Mobile Devices

### Description

TCXO7500Z-48MHz-A-V offers high frequency stability and reliable short and long term stability in a compact SMD package, which is suitable for microwave communication and mobile applications.

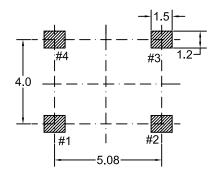
### Mechanical Drawing & Pin Connections



**Drawing No:** 

MD1) 00+) !\*

### Solder pattern



Pin Function

#1 Vc(EFC)

#2 GND

#3 Output

#4 Vcc

Unit in mm 1mm = 0.0394 inches



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# **Specifications**

Oscillator	Sum	Condition	Value			Heit	Note
Specification	Sym	Condition	Min.	Тур.	Max.	Unit	Note
Nominal Frequency	Fo			48		MHz	
Output Waveform				CMOS			
Output High	V <sub>OH</sub>		≥0.9V <sub>CC</sub>		V <sub>cc</sub>		
Output Low	V <sub>OL</sub>		≤0.1V <sub>CC</sub>		V <sub>cc</sub>		
Output Load					15	pF	
Rise / Fall Time				<3		ns	
Symmetry (Duty)		@ ½ V <sub>CC</sub>	45		55	%	
Start-up Time				<5		ms	
Power Supply							
Supply Voltage	Vcc			+3.3		V	
Current Consumption			<6		mA		
Frequency Adjustment Range							
Electronic Frequency Control (EFC)				∆F >±5		ppm	
EFC Voltage	Vc	±1.0V	1.5		V		
EFC Slope			Positive				
Frequency Stability							
Vs Operating Temperature		0 4000 to 10500		<.0.0			
Reference to (F <sub>MAX</sub> + F <sub>MIN</sub> ) / 2		Over -40°C to +85°C	≤±0.3			ppm	
Vs Supply Voltage changes	V <sub>S</sub> ±5%		<±0.1		nnm		
Reference to frequency at nominal supply	VS	±5%		≥±0.1		ppm	
Vs Load changes		±10%		≤±0.1		ppm	
Reference to frequency at nominal load				≥±0.1		ррпі	
Vs. Aging		1 <sup>st</sup> year		≤±1.0		ppm	
Short Term Stability ADEV		$\tau$ = 1 sec		1 x 10 <sup>-10</sup>			
Frequency Tolerance ex-factory		@ +25°C		≤±1.0		ppm	
		@ 100 Hz		-110			
Phase Noise @ 49MH7		@ 1 KHz		-135		dBc / Hz	
Phase Noise @ 48MHz		@ 10 KHz	-150		ubc/nz		
		@ 100 KHz		-155			
Environmental Conditions							
Operating Temperature Range	-40°C to +85°C						
Storage Temperature Range	-55°C to +105°C						
Reflow Profile as per IPC/JEDEC J-STD-02	≤ 260°C over 10 sec max.						
Moisture Sensitivity		Level 1 (unlimited)					

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### **Environmental Conditions**

Test	IEC 60068 Part	IEC 60679-1 Clause	MIL-STD- 202G Method	MIL-STD- 810F Method	MIL-PRF- 55310D Clause	Test Conditions (IEC)
Sealing tests (if applicable)	2-17	5.6.2	112E		3.6.1.2	Gross leak: Test Qc, Fine leak: Test Qk
Solderability Resistance to soldering heat	2-20 2-58	5.6.3	208H 210F		3.6.52 3.6.48	Test Ta Method 1 Test Td <sub>1</sub> Method 2 Test Td <sub>2</sub> Method 2
Shock	2-27	5.6.8	213B	516.4	3.6.40	Test Ea, 3 x per axes 100g, 6 ms half-sine pulse
Vibration sinusoidal	2-6	5.6.7.1	201A 204D	516.4-4	3.6.38.1 3.6.38.2	Test Fc, 30 min per axes, 1 oct / min 10 Hz – 55 Hz 0,75mm; 55 Hz – 2 kHz, 10g
Vibration, random	2-64	5.6.7.3	214A	514.5	3.6.38.3 3.6.38.4	Test Fdb
Endurance tests - aging - extended aging		5.7.1 5.7.2	108A		4.8.35	30 days @+85°C 1000h, 2000h, 8000h @ +85°C

### **Test Circuit**

